

Application No.: 09/998,758
Filed: November 30, 2001
Inventor(s):
Dale E. Gulick

Examiner: Vo, Tim T.
Group/Art Unit: 2112
Atty. Dkt. No: 5500-73700

**Title: SWITCHING I/O NODE
FOR CONNECTION IN A
MULTIPROCESSOR
COMPUTER SYSTEM**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Stephen J. Curran

Printed Name _____

Printed Name _____
Signature _____ Date 7/28/07

**RESPONSE TO OFFICE ACTION OF
May 4, 2004**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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AUG 06 2004

Technology Center 2100

This paper is submitted in response to the Office Action of May 4, 2004, to further highlight why the application is in condition for allowance.

IN THE CLAIMS: